

Electronic Acknowledgement Receipt

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Application Number:	10587446
International Application Number:	
Confirmation Number:	5371
Title of Invention:	Flat optical circuit, design method for wave motion propagating circuit and computer program
First Named Inventor/Applicant Name:	Takashi Saida
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	14321_89_IDS_transmittal.pdf	95277	no	3

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10	Foreign Reference	14321_89_jp2002-090561.pdf	1022518	no	18
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11	NPL Documents	14321_89_search_opinion.pdf	623311	no	12
Warnings:					
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12	NPL Documents	14321_89_article_1.pdf	477820	no	6
Warnings:					
Information:					
13	NPL Documents	14321_89_article_2.pdf	596427	no	6
Warnings:					
Information:					
14	NPL Documents	14321_89_article_3.pdf	458136	no	7
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15	NPL Documents	14321_89_article_4.pdf	117752	no	2
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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